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CENTRAL FAX CENTER**JUN 13 2006****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Number	10/759483	Docket Number	CNTR.2232
Filed	1/16/2004	Group Art Unit	2186
Examiner	Ryan Dare	Customer No.	23669
Application Title	MICROPROCESSOR WITH VARIABLE LATENCY STACK CACHE		
First Named Inventor	Rodney E. Hooker		

FACSIMILE COVER SHEET

To: Commissioner for Patents - Central Fax Number
Fax Number: 571-273-8300

From:
Fax Number: 512-233-0850

Pages: 16 (including this cover sheet)

Dear Sir:

Please accept the attached correspondence for the above-identified matter. I hereby certify under 37 CFR 1.8 that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date of signature shown below.

Respectfully submitted,
/E. Alan Davis/

By: _____

E. ALAN DAVIS
Registration No. 39,954
Tel: (512) 301-7234
6/13/2006

Date: _____

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RESPONSE A – AMENDMENT

VIA FACSIMILE: 571-273-8300

Mail Stop **Amendment**
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Official Action of 3/13/2006, with a shortened statutory period of response set to expire 6/13/2006, please amend the above identified application as set forth below.

Amendments to the Specifications begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 8 of this paper.

Conclusions begin on page 15 of this paper.